

31033 U.S. PTO
09/855590

CLASSIFICATION
257 758

CLASS
257 758

SUBCLASS
758

ISSUE CLASSIFICATION



PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. *J.A. [Signature]*

PATENT DATE

SCANNED *Dec 2 on Am*

APPLICATION NO. 09/855590

CONT/PRIOR F

CLASS 257 758

SUBCLASS 758

ART UNIT 2814

EXAMINER T. [Signature]

APPLICANTS Shinji Fukasawa

TITLE Semiconductor device having a multiple layer wiring structure, wiring method, wiring device, and recording medium

PTO-2040 12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
257	758	257	773, 776
INTERNATIONAL CLASSIFICATION			
H01L 23/48			
Continued on Issue Slip Inside File Jacket			

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DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
(Assistant Examiner) _____ (Date) _____ (Primary Examiner) _____ (Date) _____ (Legal Instruments Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
			ISSUE FEE	
			Amount Due	Date Paid
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